



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:  
Chamberlain, et. al.

Serial No.: 10/814,736

Filed: 3/31/2004

Title: Memory Allocation Using Mask-Bit  
Pattern to Encode Metadata within  
Memory Address (as amended)

§ Confirmation No. 7236  
§ Notice of Allowance  
§  
§ Examiner: Campos, Yaima  
§  
§ Intellectual Property Law Department  
§ International Business  
§ Machines Corporation  
§ 11400 Burnet Road  
§ Austin, Texas 78758  
§

**TRANSMITTAL DOCUMENT**

Box Issue Fee  
Commissioner for Patents  
Washington, D.C. 20231

Sir:

Transmitted herewith are the following:

- ☒ Issue Fee Transmittal Form PTOL-85B [2 copies]
- ☒ Fee Address Indication Form PTO/SB/47
- ☒ Change of Correspondence Address Form PTO/SB/122
- ☒ A self-addressed stamped postcard to be returned to sender.

Date: September 22, 2006

Respectfully submitted,

By Leslie A. Van Leeuwen  
Leslie A. Van Leeuwen  
Attorney for Applicant  
Registration No. 42,196

P.O. Box 90609  
Austin, Texas 78709-0609  
Telephone: (512) 301-6738  
Facsimile: (512) 301-6742  
Email: jvl@austin.rr.com